Proceedings of the 13th USENIX Symposium
on Networked Systems Design and Implementation (NSDI '16)

Errata Slip

In the paper "Be Fast, Cheap and in Control with SwitchKV" by Xiaozhou Li and Raghav Sethi, *Princeton University;* Michael Kaminsky, *Intel Labs;* David G. Andersen, *Carnegie Mellon University;* Michael J. Freedman, *Princeton University* (Wednesday session, "Network Architectures and Protocols," pp. 31–44 of the proceedings), the order of authors and order of author affiliations are not consistent.

Original text:
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Revised text:
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In the paper "Maglev: A Fast and Reliable Software Network Load Balancer" by Danielle E. Eisenbud, Cheng Yi, Carlo Contavalli, Cody Smith, Roman Kononov, Eric Mann-Hielscher, Ardas Cilingiroglu, and Bin Cheyney, *Google Inc.;* Wentao Shang, *University of California, Los Angeles;* Jinnah Dylan Hosein, *SpaceX* (Friday session, "In-Network Processing," pp. 523–535 of the proceedings), Table 1 needs to be corrected as follows. We changed it from 1-indexed to 0-indexed, and also adjusted the indexing scheme of the permutation tables to make them more readable. Moreover, in the last paragraph of Section 3.4, "B2" and "row 7" has been replaced by "B1" and "row 6", respectively.

Table 1, original

$B1$	$B2$	$B3$
2	1	5
4	5	6
6	2	7
1	6	1
3	3	2
5	7	3
7	4	4

Permutation tables for the backends.

Before	After
$B2$	$B1$
$B1$	$B1$
$B2$	$B1$
$B1$	$B1$
$B3$	$B3$
$B3$	$B3$
$B1$	$B3$

Lookup table before and after $B2$ is removed.

Table 1, corrected

	$B0$	$B1$	$B2$
0	3	0	3
1	0	2	4
2	4	4	5
3	1	6	6
4	5	1	0
5	2	3	1
6	6	5	2

Permutation tables for the backends.

	Before	After
0	$B1$	$B0$
1	$B0$	$B0$
2	$B1$	$B0$
3	$B0$	$B0$
4	$B2$	$B2$
5	$B2$	$B2$
6	$B0$	$B2$

Lookup table before and after $B1$ is removed.